Master of Science (M.S.) Coursework-only Program Electrical and Computer Engineering Department

Design Verification and Validation Track

All master's students must complete a tentative degree plan, which is approved by the adviser, no later than the second quarter of residence at PSU. NOTE: all changes must be initialed and dated by both the student and the adviser. All Pre-admission and Transfer credits MUST be approved by both the ECE Department and the Graduate School. Use the columns on the right to indicate approved () Pre-admission (taken at PSU before formal admission) and () Transfer credits (taken at another institution). Master's students may apply no more than three credits of ECE 507 Graduate Seminar toward degree elective requirements. Courses with a grade of C+ or lower can be used toward elective requirements only, and only with advisor approval.

Core – 16 credits required					
ECE 571 Intro to System Verilog for Design & Verification					
ECE 585 Microprocessor System Design					
ECE 593 Fundamentals of Pre-Silicon Validation					
ECE 595 Emulation and Functional Specification Verification					
Depth and Breadth Course list - 16 credits required					
ECE 540 System On Chip Design with FPGAs					
ECE 550 Post-Silicon Functional Validation					
ECE 560 Assertion Based Verification					
ECE 581 ASIC: Modeling and Synthesis					
ECE 582 Formal Verification of Hardware/Software Systems					
ECE 586 Computer Architecture					
ECE 587 Advanced Computer Architecture I					
ECE 590 Digital Design using Hardware Description Languages					
ECE 597 Post-Silicon Electrical Validation					
One additional ECE lecture class – 4 credits required					
Write in course number and name				++	
Electives – 9 credits required					
Write in subject, course number and name					
		TOTAL CREDITS:			
Notes:					
Email:	ID#:				
	_				
Student Name:	Signature:				
(Last) (First)					
Adviser:	Signature:				
(Last) (First)	· <u>-</u>				
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ECE Graduate Program Director Signature:			Date:		