## Master of Science (M.S.) Coursework-only Program Electrical and Computer Engineering Department

## **Computer Architecture and Design Track**

All master's students must complete a tentative degree plan, which is approved by the adviser, no later than the second quarter of residence at PSU. NOTE: all changes must be initialed and dated by both the student and the adviser. All Pre-admission and Transfer credits MUST be approved by both the ECE Department and the Graduate School. Use the columns on the right to indicate approved (2) Pre-admission (taken at PSU before formal admission) and (1) Transfer credits (taken at another institution). Master's students may apply no more than three credits of ECE 507 Graduate Seminar toward degree elective requirements. Courses with a grade of C+ or lower can be used toward elective requirements only, and only with advisor approval.

	TERM	GRADE	Credits	Р	Т
Core – 16 credits required					
ECE 585 Microprocessors System Design					
ECE 586 Computer Architecture					
ECE 587 Advanced Computer Architecture I					
ECE 588 Advanced Computer Architecture II					
Depth and Breadth Course list - 16 credits required					
ECE 525 Digital Integrated Circuit Design I					
ECE 526 Digital Integrated Circuit Design II					
ECE 527 High-Performance Digital Systems					
ECE 540 System on a Chip Design with FPGAs					
ECE 544 Embedded System Design with FPGAs					
ECE 571 Introduction to System Verilog					
ECE 581 ASIC Modeling and Synthesis					
ECE 582 Formal Verification of HW/SW Systems					
ECE 583 Low Power IC Design					
ECE 593 Fundamentals of Pre-Silicon Verification					
One additional ECE lecture class – 4 credits required					
Write in course number and name					
Electives – 9 credits required					
Write in subject, course number and name					
		TOTAL			
		CREDITS:			
			1		

Email:						ID#:	
Student Name:				Signature:			
		(Last)	(1	First)			
Adviser:						Signature:	
	(Last)		( First)				
ECE Graduate Director Signature:						Date:	